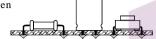
#### Soldering Hints

- 1) Put leads through mounting holes from the side with part outline. Ensue component evenly touch PCB.
- (2) Solder leads at the other side. Solder should fully fill and cover soldering pads. Avoid bridges between
- neighbering pads. (3) Cut unused leads flush with cutter.



## DSO 138 Oscilloscope DIY Kit

# User Manual Rev. 04 Applicable models: 13803K, 13804K

### Tools you need

- (1) Iron (20W) (4) Screw driver
- (2) Solder wire (5) Flush cutter

□ J4

Note:

optional.

(3) Multimeter (6) Tweezers

### Before you start

- 1 Check part values & quantities against part list
- (2) Always meter resistor values before soldering
- (3) Understand all part polarities and orientations

## Important!!!

Install all SMD parts before proceeding to Step1 if you purchased kit 13804K.

## 9. Pin header (for power)



10. Transistors

Face the opening outward

:8550

:9014

:79L05

:78L05

□ 19 : 2 Pin

□ Q1

□ 02

□ U4

□ U5

Attention!

Do not mix up!

Packages are similar.

Attention!

Do not mix up!

Packages are similar.

# Assembly Main Board and LCD board (follow the order as numbered)



: 200K Ω

: 2M Ω

: 20K Ω

: 300 Ω

2. HF-Chokes

3. Diodes

4. Crystal

□ R4

□ R5

□ R6

□ R11 : 150 Ω □ R38 : 1.5K Ω

☐ R28, R40 : 470 Ω ☐ R37, R39 : 10K Ω

□ L1,L3,L4 : 100 µ H

: 1N5819

: 1N4004 (or 1N4007)

: 8MHz

Cathode

□ D1

□ D2

☐ Y1

## Tact Switches

5. USB Socket \*



☐ SW4, SW5, :6 X 6 X 5mm SW6, SW7, SW8

This connector is

USB mini -B



## Ceramic Capacitors

: 330pF

: 1pF



□ C1, C9, : 0.1 µ F C10, C11, C14, C15, C16, C17, C18, C20, C23

: 120pF

□ C2 □ C3

□ C5

☐ C7, C8

: 3pF

## ☐ C12, C13 : 22pF

☐ C4, C6 : 5 - 30pF



Solder positive pole (the longer lead) to the square pad



□ D3 :φ3mm, green

11. Regulators



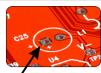
12. Capacitor trimmers

## 13. Power inductor



 $\prod L2$ : 1mH/0.5A

# 14. Electrolytic capacitors





Solder positive pole (the longer lead) to the square pad

□ C19, C21, : 100 µ F/16V C22, C24, C25, C26

### 15. Power connector



□ J10 :DC005

## 16. Pin-header (male) \*



:1 X 3 pin ☐ J6 :1 X 4 pin

Note:

These pin-headers are optional.

## 17. Pin-header (female)





□ J7, J8 : 1 X 2 pin □ J3 : 2 X 20 pin

## 18. Slide switches



☐ SW1, SW2, : 2P3T

### 19. BNC connector



: BNC □ J1

Note:

The thicker pins need to heat up longer to get good soldering result.





- 1) Make a small ring with a lead cut-off.
- 2) Solder the ring to the two holes of J2 (as shown in the photo).







Note: Install to the side opposite to LCD panel.

□ J1 2 X 20 pin ☐ J2, J3 :1 X 2 pin

Step 2 Test and Use

**NOTE:** You need a 9V DC power supply (at least 200mA capacity) to run the scope. This power supply is not included in the kit.

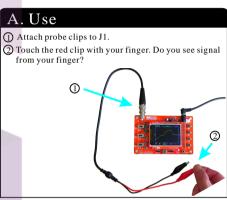
The assembly should look like t after you have finished all part

## A. Check voltages

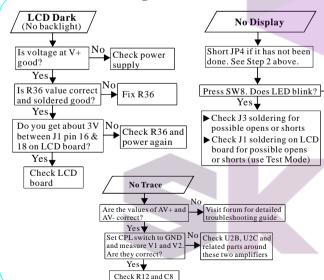
- (1) Apply 9V power to J10 (or J9).
- (2) Check voltage at TP22. It should be around +3.3V.
- (3) If voltage at TP22 is good disconnect power. Short JP4 with solder permanently.

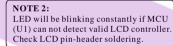












No ► Check +3.3V voltage → Check Y1, C12, C13 ► Check LED installation

Test Mode

What it is and how it works

Test Mode is used to find out possible opens (for all port pins) and shorts (for pins PB0 - 15 and PC13-15). When entered it first checks PB and PC pins with special patterns to find out possible shorts. If found LED will be fast blinking. Otherwise, it generate 3.3V and 0V alternatively at each port pins (PA, PB, PC and PD) in cycle of about 4 seconds. These signals can be used to check for opens.

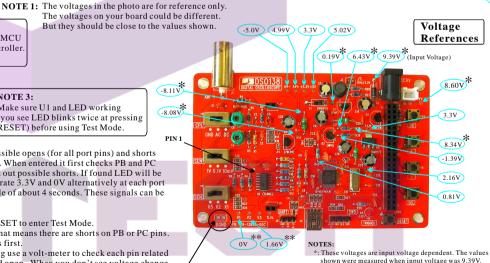
NOTE 3:

Make sure U1 and LED working

RESET) before using Test Mode.

(you see LED blinks twice at pressing

- 1. Hold down SW4 and press RESET to enter Test Mode.
- 2. If you see LED fast blinking that means there are shorts on PB or PC pins. You need to find out the shorts first.
- 3. If you see LED slowly blinking use a volt-meter to check each pin related connections that are suspected open. When you don't see voltage change at a spot which is supposed being connected to a port pin there may be open between the spot and the port pin.



Place the negative pen of volt-meter

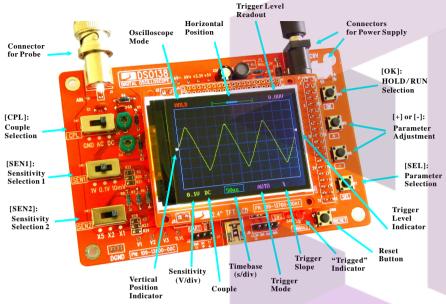
here to do voltage measurements.

Tech Support: www.jyetech.com/forum

\*\*: These voltages are measured when CPL switch (SW1)

#### – How to Use -

## **Display and Controls**



### **Connections**

Power Supply: Connect DC power supply to J9 or J10. The power supply voltage must be in the range of 8 - 12 V.

Probe: Connect probe to J1.

#### Attention

- Power supply voltage must not exceed
   12V. Otherwise U5 will get hot.
- Allowed maximum signal input voltage is 50Vpk (100Vpp) with the clip probe.

## **Operations**

Press on [SEL] button: Select parameter to be adjusted. The selected parameter will be highlighted.

Press on [+] or [-] button: Adjust the parameter selected by [SEL] button.

Press on [OK] button: Freeze waveform refresh (entering HOLD state). Press on it again will de-freeze.

Change [CPL] switch: Set couple to DC, AC, or GND. When GND is selected the scope input is isolated from input signal and connected to ground (0V input).

Change [SEN1] or [SEN2] switch: Adjust sensitivity. The product of [SEN1] and [SEN2] settings makes the actual sensitivity which is displayed at the lower-left corner of the panel.

Press on [Reset] button: Perform a system reset and re-boots the oscillscope.

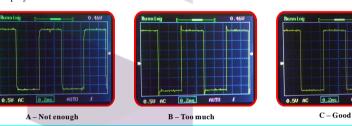
#### **0V** Line Alignment

Sometimes you may find the 0V line (the trace corresponding to 0V input voltage) does not match with the VPOS indicator at the screen left border. This can easily be fixed by performing the "0V line alignment" function. First, set the couple switch [CPL] to GND position. Then press on [SEL] button to make VPOS indicator highlighed and hold down [OK] button for about 2 seconds. You will set the trace aligned to VPOS indicator when you release [OK] button. You may see some residue mismatch remains at the highest sensitivity settings. This is normal.

#### Probe Calibration

Because there is always some capacitance between scope input and ground probe needs to be calibrated to achieve better measurement results for high frequency signals. This can be done with the help of the built-in test signal. To do this please follow the steps below.

- 1. Connect the red clip to the test signal terminal and leave the black clip un-connected (see photo at right).
- Set [SEN1] switch to 0.1V and [SEN2] switch to X5.
   Set [CPL] switch to AC or DC.
- Adjust timebase to 0.2ms. You should see waveform similar to that shown in photos below. If traces are not stable adjust trigger level (the pink triangle on right screen border) so as you get a stable display.
- 4. Turn C4 (capacitor trimmer) with a small screw driver so that the waveform displays sharp rightangle (photo C).
- Set [SEN1] switch to 1V and [SEN2] switch to X1while keep all other settings unchanged. Adjust C6 so that sharp rightangle waveform is displayed.



#### Turn On/Off Readouts

 $Press\ [SEL]\ so\ that\ timebase\ is\ highlighted.\ Hold\ down\ [OK]\ button\ for\ about\ 2\ seconds.\ This\ will\ turn\ on/off\ measurement\ readouts.$ 

#### Waveform Save/Recall

Press [SEL] & [+] simultaneously: Save currently displayed waveform to non-volatile memory. Press [SEL] & [-] simultaneously: Recall saved waveform

#### **Triggers and Their Modes**

Triggers are events that indicate signal voltage acrossing a set level (i.e. trigger level) along a specified direction (i.e. trigger slope, rising or falling). Oscilloscope uses triggers as reference points in time for stable waveform display and measurements.

#### Auto Mode

In auto mode oscilloscope will perform display refresh no matter triggers happen or not. When triggers are detected waveform display will be displayed with reference to trigger points. Otherwise, display waveform at ramdom reference points.

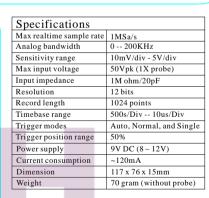
#### Normal Mode

In normal mode oscilloscope will only perform display refresh when there are triggers. If no triggers happen waveform display will stay unchanged.

#### Single Mode

Single mode is the same as normal mode except that oscilloscope will enter HOLD state after a trigger has been detected and waveform display has been updated.

Normal and single modes are useful for capturing sparse or single waveform.



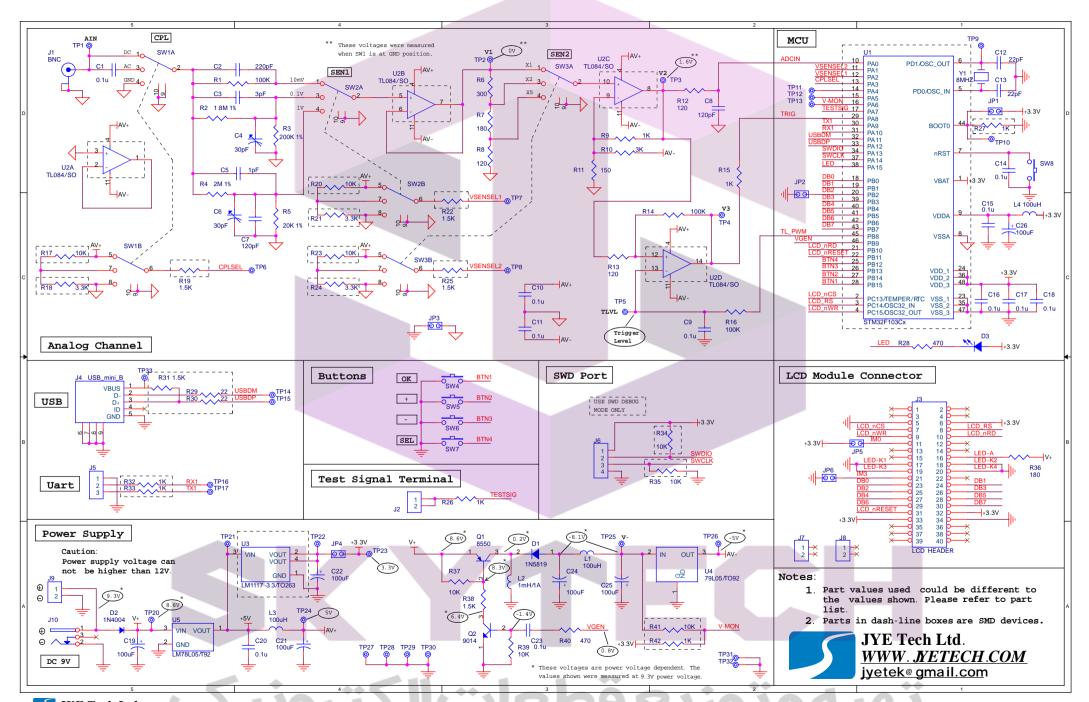
Leave black clip

Connect red clip to

test signal output

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